

#13/Pn-D
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



In re Patent Application of)
 Shunpei YAMAZAKI et al.)
 Serial No. 09/436,984) Art Unit: 2823
 Filed: November 9, 1999) Examiner: W. Coleman
 For: SEMICONDUCTOR DEVICE AND)
 MANUFACTURING METHOD)
 THEREOF) Date: September 4, 2001

PRELIMINARY AMENDMENT

Honorable Commissioner for Patents
 Washington, D.C. 20231

Sir:

Please preliminarily amend the subject application as follows:

IN THE CLAIMS:

Please amend claims 1-4 as follows: Please note that these claims are presented below in their amended form. They are further presented as an Attachment to the Amendment whereby the amendments to the claims are outlined using the conventional method of bracketing and underlining.

1. (Amended) A semiconductor device including a CMOS circuit having an NTFT and a PTFT, each of the NTFT and the PTFT including an active layer, an insulation film in contact with the active layer, and a wiring in contact with the insulation film, wherein

only the NTFT includes a side wall on a side of the wiring,

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